

J1021 U.S. PTO  
10/028326  
12/21/01

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM 10028326	FILING DATE 12/21/2001	CLASS 714	SUBCLASS 707	GAU 2104	EXAMINER T. J. VEROS
-----------------------	---------------------------	--------------	-----------------	-------------	-------------------------

\*\*APPLICANTS: Whetsel Lee;

\*\*CONTINUING DATA VERIFIED: NONE  
ju 4/29/04

\*\*FOREIGN APPLICATIONS VERIFIED: NONE  
ju 4/29/04

Best Available Copy

PG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 110 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	TI-32417
Verified and Acknowledged Examiners's initials <i>[Signature]</i>	
TITLE: Interconnections for plural and hierarchical P1500 test wrappers	

U.S. DEPT. OF COM. / PAT. & TM. PTO-4326 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
ISSUE FEE		Sheets Drwg.	Figs. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
PREPARED FOR ISSUE			
<input type="checkbox"/> TERMINAL DISCLAIMER		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

FILED WITH: ☐ DISK (CRF) ☐ CD-ROM  
(Attached in pocket on right inside flap)